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APPLICATION NO.	FI	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/773,523	(	02/02/2001	Chun Chen	M4065.0390/P390	6271	
24998	7590	09/11/2002				
		RO MORIN &	EXAMINER			
2101 L STRI WASHINGT		20037-1526		BEREZNY, NEAL		
				ART UNIT	PAPER NUMBER	
			2823			
				DATE MAILED: 09/11/2002		

Please find below and/or attached an Office communication concerning this application or proceeding.

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,	•	Application No.	Applicant(s)	
	Office Action Summary	09/773,523	CHEN, CHUN	
	omec Action Summary	Examiner	Art Unit	
<del></del>	The MAILING DATE of this communication app	Neal Berezny	2823	
A SH THE   - External after   - If the   - If NO   - Failure   - Any rearner	ORTENED STATUTORY PERIOD FOR REPLIMAILING DATE OF THIS COMMUNICATION. Insions of time may be available under the provisions of 37 CFR 1.1 SIX (6) MONTHS from the mailing date of this communication. In period for reply specified above is less than thirty (30) days, a replication of the provision	Y IS SET TO EXPIRE 3 MON  36(a). In no event, however, may a reply y within the statutory minimum of thirty (3 will apply and will expire SIX (6) MONTHS	NTH(S) FROM  be timely filed  o) days will be considered timely.  from the mailing date of this comn	
Status	B			
1)⊠	Responsive to communication(s) filed on 01 A			
2a)☐		is action is non-final.		
3) 🗌 Dispositi	Since this application is in condition for allowationsed in accordance with the practice under on of Claims	ance except for formal matter Ex parte Quayle, 1935 C.D. 1	s, prosecution as to the n 11, 453 O.G. 213.	nerits is
4) 🖾	Claim(s) 1-50 is/are pending in the application			
•	4a) Of the above claim(s) <u>25-50</u> is/are withdraw	n from consideration.		
5)[	Claim(s) is/are allowed.			
6)⊠	Claim(s) <u>1-24</u> is/are rejected.			
7)🖂	Claim(s) <u>8,12,14,15,19,23 and 24</u> is/are object	ed to.		
	Claim(s) are subject to restriction and/or on Papers	r election requirement.		
9)□ T	The specification is objected to by the Examiner	·.		
	he drawing(s) filed on <u>02 February 2001</u> is/are		ed to by the Examiner	
	Applicant may not request that any objection to the			
11) 🔲 T	he proposed drawing correction filed on		• •	
	If approved, corrected drawings are required in rep		,	
12) 🗌 T	he oath or declaration is objected to by the Exa	aminer.		
Priority u	nder 35 U.S.C. §§ 119 and 120			
13) 🗌 📝	Acknowledgment is made of a claim for foreign	priority under 35 U.S.C. § 11	9(a)-(d) or (f)	
	All b) Some * c) None of:		(-) (-)	
	I. Certified copies of the priority documents	have been received.		
2	2. Certified copies of the priority documents		cation No.	
	Copies of the certified copies of the priori application from the International Bure se the attached detailed Office action for a list o	ty documents have been rece eau (PCT Rule 17.2(a)).	eived in this National Sta	је
	knowledgment is made of a claim for domestic	·		olication).
a)	☐ The translation of the foreign language proveknowledgment is made of a claim for domestic	risional application has been	received.	
\ttachment(		. ,		
Notice Notice Notice	of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-948) tion Disclosure Statement(s) (PTO-1449) Paper No(s) 2.	4) Interview Sumn 5) Notice of Inform 6) Other:	nary (PTO-413) Paper No(s) nal Patent Application (PTO-152	<u></u> ·
Patent and Trad O-326 (Rev.		on Summary	Part of Pap	er No. 5

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### **DETAILED ACTION**

### Election/Restrictions

1. Examiner acknowledges applicant's election of the group I invention, claims 1-24, without traverse.

## **Drawings**

2. Figure 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

## Claim Objections

3. Claims 8, 12, 14, 15, 19, 23, and 24 are objected to under 37 CFR 1.75(c), as being of improper dependent form for failing to further limit the subject matter of a previous claim. Applicant is required to cancel the claim(s), or amend the claim(s) to place the claim(s) in proper dependent form, or rewrite the claim(s) in independent form. The parent claims already contain the limitation that the implant step follows both the remove step and the forming step. The dependent claims either repeat these limitations or change the order, which in either case is not further limiting the parent claims.

# Claim Rejections - 35 USC § 112

- 4. The following is a quotation of the second paragraph of 35 U.S.C. 112:

  The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 5. Claim 24 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant

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regards as the invention. It is unclear how the second doped layer is provided before the first when claim 20, the parent claim, specifies that the first layer be before the second.

## Claim Rejections - 35 USC § 102

6. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 7. Claims 20-22, and 24 are rejected under 35 U.S.C. 102(b) as being anticipated by Chen et al. (5,482,881). Chen teaches a method of forming a source region in a substrate, fig.3, el.300, comprising forming a pair of gate structures which extend in a first direction over a substrate, el.700, altering the upper surface profile of said substrate to form alternating areas of higher substrate surface elevation and areas of lower substrate surface elevation along said first direction and between said pair of gate structures, fig.3, el.300, providing a first doped layer in said substrate between said gate structures, which has a profile which follows that of said upper surface profile and providing a second doped layer in said substrate between said gate structure, which is below said first doped layer and which has a profile which follows that of said first doped layer, fig.4d, el.130, wherein at least one of said areas of higher and lower substrate surface elevation is doped by said first doped layer to act as a source region of a transistor, fig.3, el.300, wherein said area of higher substrate surface elevation acts as a

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source region, fig.3, el.300, wherein said second doped layer is provided in said substrate before said first doped layer, col.6, ln.55-63.

## Claim Rejections - 35 USC § 103

- 8. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 9. Claims 1-19, and 23 are rejected under 35 U.S.C. 103(a) as being unpatentable over Chen et al. (5,482,881) as applied for claims 20-22, 24 above. Chen teaches a method of forming a plurality of dopant pockets on a substrate, fig. 1, el. 130, 116, comprising forming a plurality of implantable regions on said substrate separated by field oxide regions, Fig.3, el.300, said implantable regions and field oxide regions extending in a first direction, forming a plurality of word lines located over said implantable regions and field oxide regions, el.700, said word lines extending in a second direction perpendicular to said first direction, selectively etching, col.6, In.55-60, and removing portions of said field oxide regions between two adjacent word lines to expose respective substrate regions, el.302, col.6, ln.16-22, forming source regions in said implantable regions, fig.4d, el.132, co.7, ln.1-5, implanting a dopant into said substrate through said respective substrate regions to form said dopant pockets beneath said source regions, el. 130, col.6, In.55-63, wherein said dopant is a n-type dopant, col.6, In.55-60, wherein each of said word lines is formed of a gate stack comprising a gate oxide, a floating gate, a dielectric formed over said floating gate, and

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a control gate formed over said dielectric, el.120, 122, 124, and 126, wherein said act of implanting said dopant is carried out with an implanting energy higher than implanting energy used to form said source regions, col.6, ln.55-63, said act of implanting said dopant employs directing said dopant through said substrate region at an angle of substantially 90 degrees incidence to said substrate region, fig.6c, el.MDD1, wherein said act of implanting said dopant employs directing said dopant through said substrate region at angles different than substantially 90 degrees incidence to said substrate region, fig.8d, el.MMD1, wherein said act of implanting boron into said substrate is carried out after said act of removing said field oxide material, fig.3, el.302,130.

10. Chen does not appear to specifically state that said act of implanting said dopant into said substrate is carried out after said act of forming said source regions, nor the use of a p-type dopant, such as Boron, nor employing a BF<sub>2</sub> dopant source. Chen does teach the use of a generic "first conductivity type", col.11, claim 4, thereby suggesting to one skilled in the art that Chen anticipated the use of both N-type and P-type dopants. Official Notice is given that it is well known in the art to use different dopant types to build both NMOS and PMOS devices by using both types of dopants, in order to build CMOS devices having lower power consumption. Further, official notice is given that Boron is a well-known P-type dopant and that BF<sub>2</sub> is a well-known dopant source. It would be obvious to one of ordinary skill in the art at the time of the invention to use boron and BF<sub>2</sub> for a P-type dopant because of its compatibility with silicon crystal structure as a doped semiconductor. Finally, it would be obvious to one of ordinary skill in the art at the time of implants from deep and

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then shallow to shallow and then deep. Both methods are well known in the art to be equivalent processes and a skilled artisan would be motivated to employ either method in order to provide greater process latitude when building and designing a process for peripheral devices that may require a shallow implant first so that a masking or etching step could be performed prior to the deep implant, thereby allowing the same implant to be performed on both devices reducing process steps and costs.

#### CONCLUSION

11. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Neal Berezny whose telephone number is (703) 305-1481. The examiner can normally be reached on Monday to Friday from 9:00 to 5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy, can be reached at (703) 308-4918. The fax phone number for the organization where this application or proceeding is assigned is (703) 308-7724.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

SUPERVISORY PRIMARY EXAMINER
TECHNOLOGY CENTER 2800

Neal Berezny

Patent Examiner

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